

FIG. 1: prior art

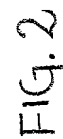
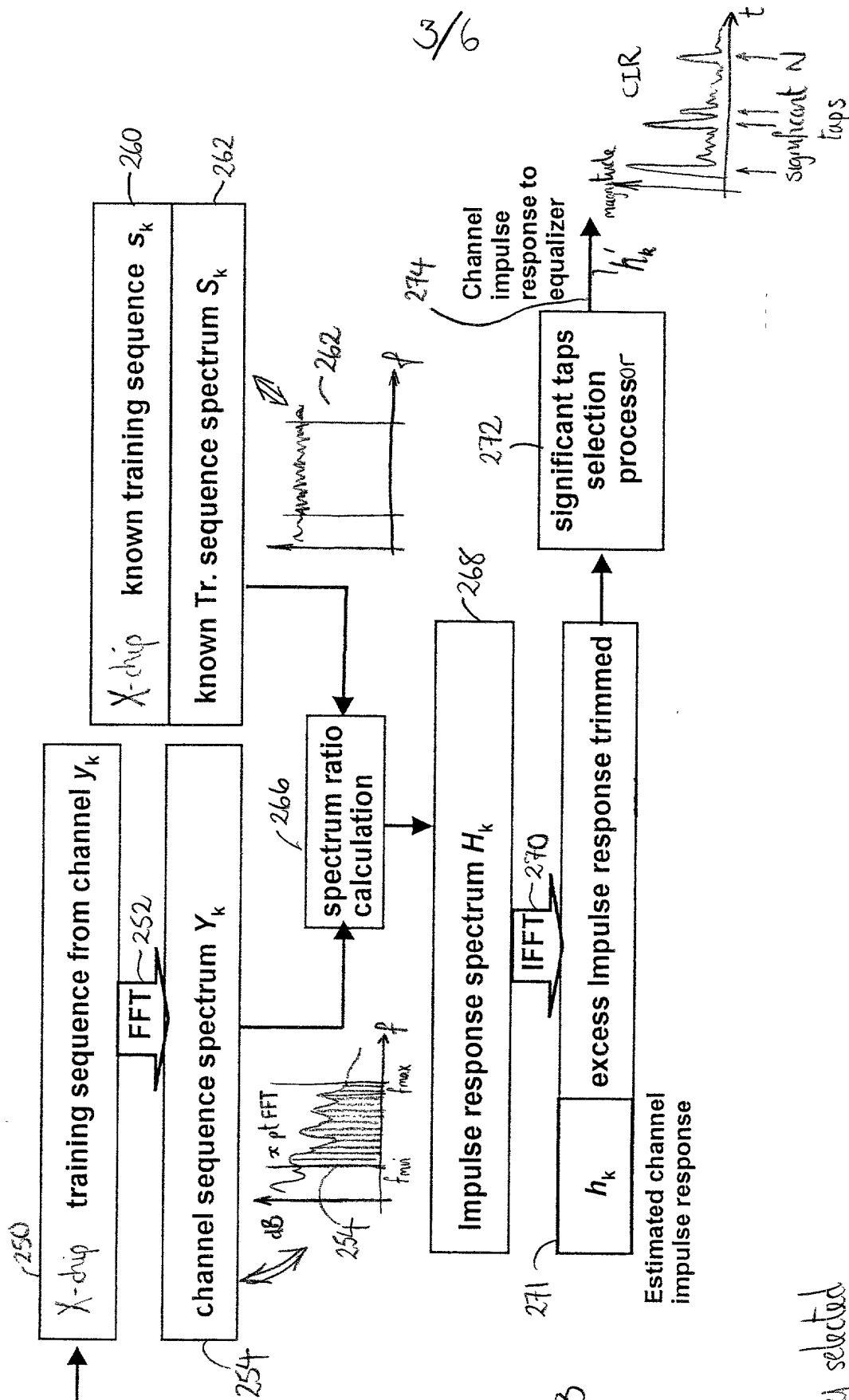
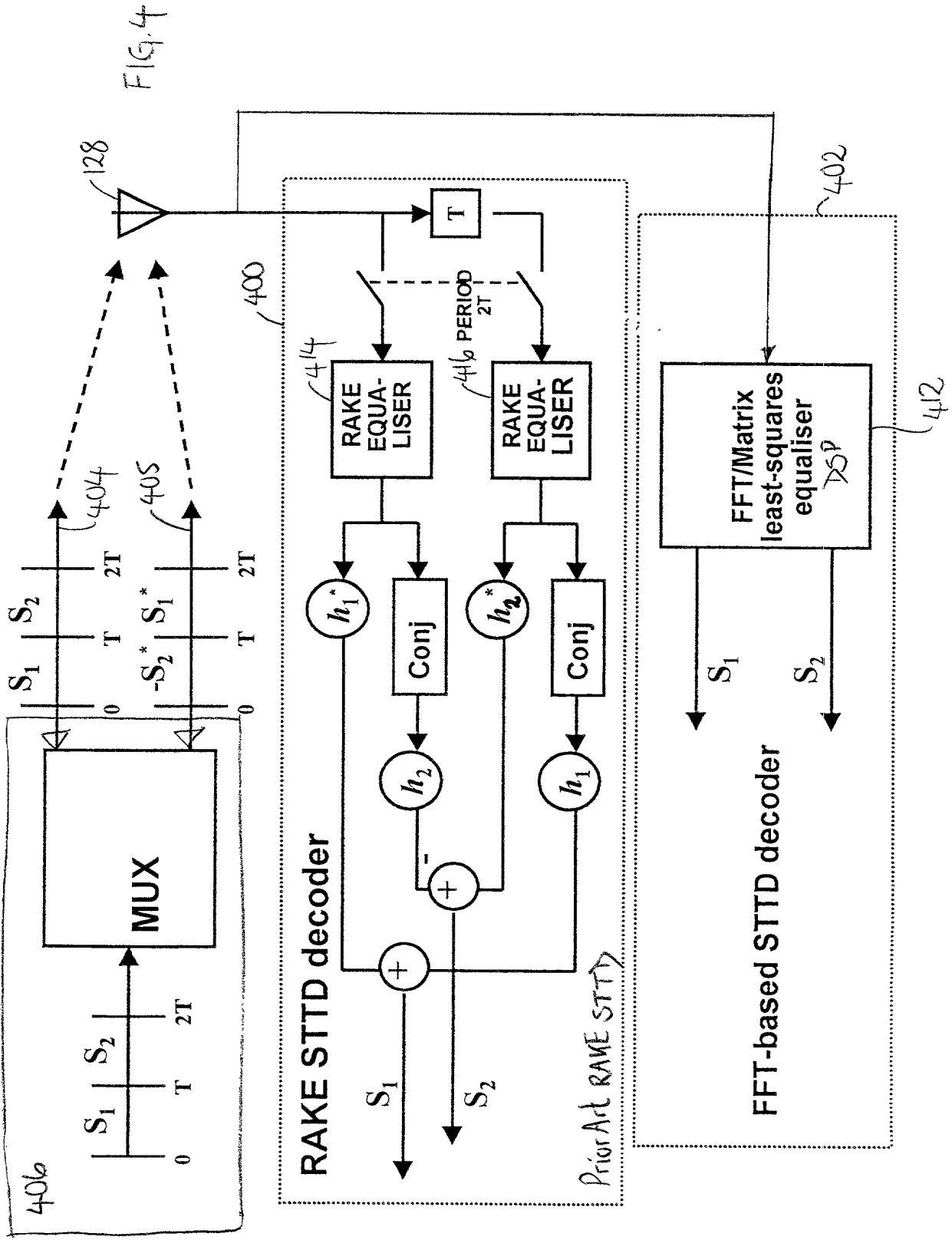


FIG. 2



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X=arbitrary selected number of chips in training sequence set by system standard.



$h_1^r(0) + 0$	$0 - h_2^r(0)$	$-h_1^i(0) + 0$	$0 - h_2^i(0)$	0
$h_1^r(1) + h_2^r(0)$	$h_1^r(0) - h_2^r(1)$	$-h_1^i(1) + h_2^i(0)$	$-h_1^i(0) - h_2^i(1)$	0
$h_1^i(0) + 0$	$0 - h_2^i(0)$	$h_1^r(0) - 0$	$0 + h_2^r(0)$	0
$h_1^i(1) + h_2^i(0)$	$h_1^i(0) - h_2^i(1)$	$h_1^r(1) - h_2^r(0)$	$h_1^r(0) + h_2^r(1)$	0
$h_1^r(2) + h_2^r(1)$	$h_1^r(1) - h_2^r(2)$	$-h_1^i(2) + h_2^i(1)$	$-h_1^i(1) - h_2^i(2)$	0
$h_1^r(3) + h_2^r(2)$	$h_1^r(2) - h_2^r(3)$	$-h_1^i(3) + h_2^i(2)$	$-h_1^i(2) - h_2^i(3)$	0
$h_1^i(2) + h_2^i(1)$	$h_1^i(1) - h_2^i(2)$	$h_1^r(2) - h_2^r(1)$	$h_1^r(1) + h_2^r(2)$	0
$h_1^i(3) + h_2^i(2)$	$h_1^i(2) - h_2^i(3)$	$h_1^r(3) - h_2^r(2)$	$h_1^r(2) + h_2^r(3)$	0
$h_1^r(4) + h_2^r(3)$	$h_1^r(3) - h_2^r(4)$	$-h_1^i(4) + h_2^i(3)$	$-h_1^i(3) - h_2^i(4)$	0
$h_1^r(5) + h_2^r(4)$	$h_1^r(4) - h_2^r(5)$	$-h_1^i(5) + h_2^i(4)$	$-h_1^i(4) - h_2^i(5)$	0
$h_1^i(4) + h_2^i(3)$	$h_1^i(3) - h_2^i(4)$	$h_1^r(4) - h_2^r(3)$	$h_1^r(3) + h_2^r(4)$	0
$h_1^i(5) + h_2^i(4)$	$h_1^i(4) - h_2^i(5)$	$h_1^r(5) - h_2^r(4)$	$h_1^r(4) + h_2^r(5)$	0
....

Fig. 5 Layout of channel impulse responses in \tilde{c}

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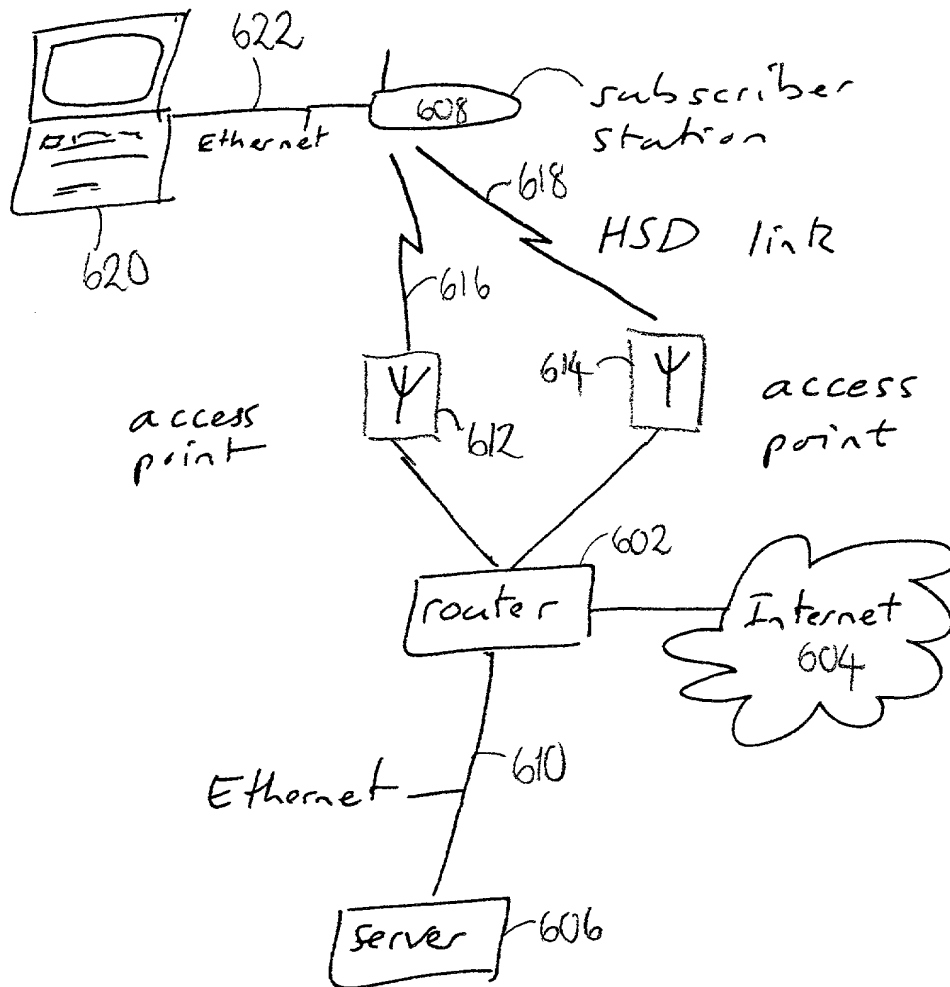


FIG. 6